

Rubidium Series

TIMING SOLUTIONS



RUB XV-I

IRIG-B Reader Programmable GPI Inputs/Outputs



Option "I" Version: 1.3 December 2, 2020





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A1 Revision History

No.	Date	Subject
0.n		Preliminary documents, changes without notice.
1.0	August 19, 2016	First released document.
1.1	May 10, 2019	Added date formats and TC_Link.
1.2	September 30, 2019	Changed address of Plura Europe GmbH.
1.3	December 2, 2020	Re-formatted in new design.

A2 Copyright

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A3 General Remarks

This manual is a supplement to the "Functional Description & Specifications" of the XV appropriate module.

It describes those additional features which are made available assembling an additional board and flashing a special firmware with OPT75.



1 Hardware

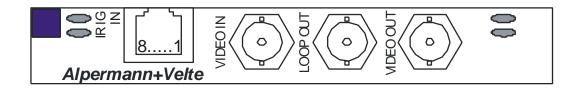
1.1 Additional Board

Option "I" requires assembling an additional board.

The rear plate of the module receives a RJ45 jack additionally.

This board can be assembled for RUB1 as well as for RUB3 versions of XV.







1.2 Connections

Pin assignments

IRIG IN RJ45 jack	1: GPI_1 2: GPI_2 3: IRIG_IN_A 6: IRIG_IN_B 4: GND 5: do not connect
	7: GPI_3 8: GPI_4

Signal descriptions

<u>orginal descriptions</u>			
GND	Signal ground.		
IRIG_IN_A IRIG_IN_B	IRIG-B (Time Code) input.		
	Unbalanced IRIG-B Cinch/RCA/BNC Pin 3: REF_IN_A— Signal + Pin 6: REF_IN_B— Signal - Pin 4: GND — GND		
GPI_1 GPI_4	General Purpose In/Out: inputs or outputs for digital signals according to application. Programmable functions.		



1.3 Specifications

IRIG-B Input

Supported Formats	IRIG-B: Amplitude modulated 1 kHz carrier signal, balanced.	
	IRIG-B 123 or IRIG-B 127 according to IRIG STANDARD 200-04; AFNOR time code according to AFNOR NF S 87-500.	
Connector	RJ45: Signals IRIG_IN_A/IRIG_IN_B	
Input	Balanced or unbalanced signals Impedance: 680Ω	
	Mark Amplitude: 1 Vpp to 5 Vpp Space Amplitude: 0.5 Vpp to 2.5 Vpp Mark-to-Space Ratio: 2/1 to 6/1	

GPI

GPI_1 GPI_4: Input specification	Input "Low": -2.0 to $+1.0$ V Input "High": $+3.0$ to $+24.0$ V Impedance: ≈ 4.7 k Ω Frequency: $0 - 1$ MHz	
GPI_1 GPI_4: Output specification	Open Collector output of an NPN transistor at 4k7 pull-up resistor (5 VDC). Max. power dissipation: 200 mW. "High" state: 4.3 V (no load).	
	"Low" state: output switched to GND. Max. collector current: 100 mA DC, fused by a 100 mA autorecovery fuse. Collector-emitter saturation voltage: @100 mA: typ. 200 mV (≤ 600 mV), @10mA: typ. 90 mV (≤ 250 mV).	
	Frequency: 0 - 150 kHz.	



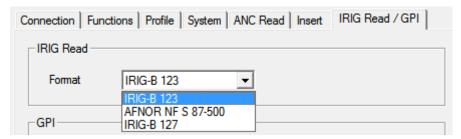
2 Functions

2.1 IRIG-B Reader

Option "I" adds IRIG-B reader functionality to the module:

• Read IRIG-B and visibly insert time and user data.

At the **IRIG Read / GPI** page of the configuration tool, three formats can be selected. Please select according to the format of the input signal.



Decoding a date makes the difference:

IRIG-B 123: This format according to IRIG STANDARD 200-04 does not include information about the current year. So, the year will be read out of the battery buffered real time clock of the module after power has turned on. It is possible to set the year manually utilizing the configuration tool. Turn of the year will be proceeded automatically. This format conveys "day of year". If the year is known, day and month can be calculated.

IRIG-B 127: This format according to IRIG STANDARD 200-04 includes year information (two digits 00 - 99, meaning 2000 - 2099). Together with the "day of year" information the date can be calculated.

AFNOR NF S 87-500: The format according to NF S 87-500 includes day, month, and year information. So, the date can be completely decoded.

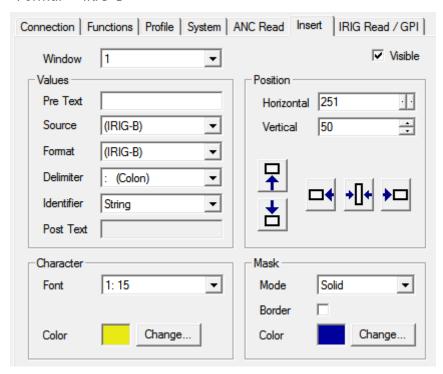


2.2 IRIG-B Inserter

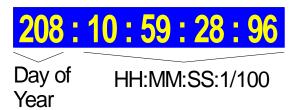
Option "I" enables to insert values from the IRIG-B reader visible to video. The module has to receive the following set-up at the **Insert** page:

"Source = IRIG-B"

"Format = IRIG-B"



Data will be displayed in the following format:

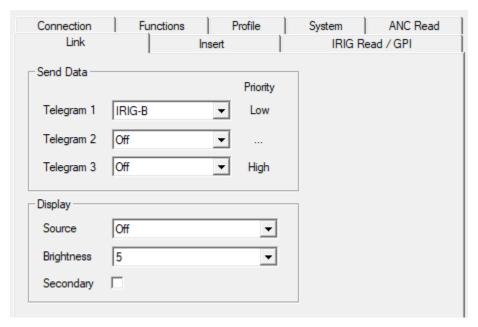


It's also possible to use the "Date" formats like "Date, DD MM YYYY" to insert local date if provided by IRIG source.



2.3 TC_Link

IRIG-B data can be distributed to other video inserters over TC_Link. This allows to insert the same IRIG-B code to different video streams.

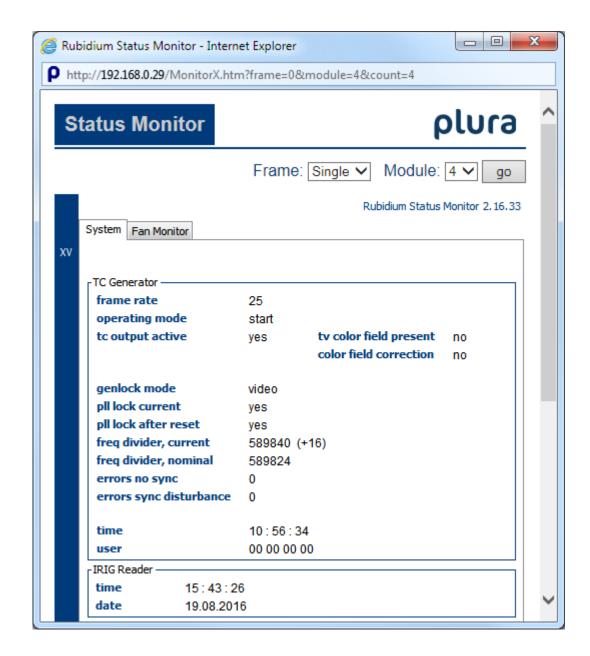


If another video module like XV receives IRIG-B data over TC_Link it will use it as its IRIG-B data source for the IRIG-B inserter.



2.4 Status Monitor

The function of the IRIG-B reader can be monitored in the "Status Monitor". On "System" page in "IRIG Reader" box it shows current time and (if an IRIG format with date was selected) current date.

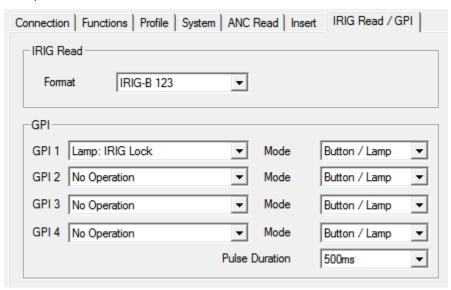




2.5 GPI

Option "I" adds GPI functionality to the module. Set-up regarding GPI functions works same way as provided for XT modules.

 The configuration tool enables the IRIG READ / GPI function to assign functions to the GPI ports.



Name (in drop- down list)	Functional description	
IRIG-B Lock	Input signal status.	
	On:	IRIG-B with valid data. Data of consecutive IRIG frames are consistent, for example there is no discontinuity of any time information.
	Slowly flashing:	IRIG-B with valid data. The check of consistent data is still in progress or the check fails.
	Off:	No IRIG-B with valid data. Either the signal has failed completely, or the cable has been disconnected, or the signal contains invalid data.

Please refer to "Functional Description & Specifications" manual of the XV module for details of configuration features.

Please refer to the document "RUB AT/DT/HT/XT Application: GPI Functions" for a description of the available functions. You can download it from:

https://plurainc.com





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